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P17856.A03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : M. ASAI et al.



Group Art Unit: 2835

Appl. No. : 09/319,258

Examiner: UNKNOWN

Filed : June 11, 1999

For : PRINTED WIRING BOARD AND METHOD
FOR MANUFACTURING THE SAME

SUPPLEMENTAL PRELIMINARY AMENDMENT

Assistant Commissioner of Patents
Washington, D.C. 20231

RECEIVED

SEP 16 1999

Sir:

TECHNOLOGY CENTER 2800

Further to the Supplemental Amendment filed June 11, 1999, and prior to an examination of the above-identified patent application, the Examiner is respectfully requested to amend the application as follows:

IN THE CLAIMS

Please amend claims 4 and 21 as follows:

31 ~~Sup 10~~ 4. (Twice Amended) A printed circuit board according to claim 1, wherein the roughened layer is formed on at least a part of [the] a side face of the conductor circuit.

32 ~~Sup 10~~ 21. (Twice Amended) A printed circuit board according to claim 13, wherein the alignment mark is used for [positio ning in the] ~~positioning during~~ mounting of a printed circuit board packaged [a s emiconductor] as a semiconductor element to another printed circuit board.